KLCS-47E, 47, 470, 470A Series

■ Features

	47E Series	47 Series	470 Series	470A Series		
Maximum ROM size	4K	× 8	8K × 8	$16K \times 8$		
Maximum RAM size	256 × 4	768 × 4	1024	× 4		
Minimum instruction execution time	$\begin{array}{l} 1.0 \mu s (at 8 \text{MHz}, V_{DD} = 2.7 V \sim 5.5 V) \\ 1.3 \mu s (at 8 \text{MHz}, V_{DD} = 4.5 V \sim 5.5 V) \\ 1.9 \mu s (at 8 \text{MHz}, V_{DD} = 2.7 V \sim 5.5 V) \\ 3.2 \mu s (at 8 \text{MHz}, V_{DD} = 2.7 V \sim 5.5 V) \end{array}$	1.9µs (at 4.2Mb, V _{DD} =4.5V~6V)	,	V _{DD} =4.5V~5.5V) V _{DD} =2.7V~5.5V)		
Number of instructions	9	0	92	105		
Number of interrupts	5/6		6			
Packages	DIP16~20 SOP16~28 SDIP28~42 SSOP30 QFP44	SDIP30~42 QFP44~80	SDIP28~64 QFP44~100	SDIP42~64 QFP44~80		

Basic functions

- Instructions: maximum 105, minimum instruction execution time: 1.0μs
- ROM table look-up instruction
- 5-bit to 8-bit data conversion instruction
- Subroutine nesting: maximum 15 levels
- Interrupt sources: 2 external, 4 internal
- Interval timer
- Serial interface

Additional functions

- VFT driver
- LCD driver
- LED driver
- Hold function (low power consumption mode)
- Multi-pin input/output
- D/A conversion (PWM) output
- A/D conversion input
- A/D converter input
- E²PROM
- 16-bit high-speed event counter
- On-screen display circuit
- DTMF generator
- DTMF receiver
- Watchdog timer
- Pulse generator
- Remote control pulse detector
- High-speed timer/counter
- Dual clock system
- One-time PROM

■ KLCS-47E, 47 Series Selection Guide

K	(LCS	6-47E, 47 Serie	<u>es</u>	S	iel			<u>or</u>	<u> </u>																	
			D	riv	er	SI	0	A/D	A/I	Puls	e ou	tput	Rem	₩a	Hig	DT	0.5	SD	Dual	PloH	Pa	ackag	ge	OTP	Min	<u></u>
ROM (byte)	RAM (nibbles)	Product No	LED	LCD	VFT	4-bit	8-bit	D converter) conversion input	PWM	PPG	Pulse	_	Watchdog timer	High-speed event counter	DTMF generator	Bar display	Character	al clock	ld function	SDIP(DIP)	QFP(SOP)	QFP(SSOP)	P type	num operating vo	: 2.7V, ⊚ : 2.2V)
		KMP47C101P/M	4																	•	16	16				
	KMP47C102P/M	4											•						•	_	20		•	•)	
1k	64	KMP47C103N/M	8				1							•						•	28	28		•	•	
		† KMP47E187M												•						•		16		•	•	<u> </u>
		† KMP47E186M												•						•		16		•	•)
		KMP47C201P/M	4																	•	16	16		•	•	
		KMP47C202P/M	4											•						•	20	20		•	•)
		KMP47C203N/M	8				1							•						•	28	28		•	•)
		KMP47C200BN/BF	8			1														•	42	44			•)
		* KMP47C206P/M	5									1		•						•	20	20		•		
	128	KMP47C210AN/AF			20	1														•	42	44		•		
2k	120	KMP47C212AN			20	1														•	42					
		KMP47C231AN	8				1		5				•							•	30					
		KMP47C233AN	8						1											•	42					
		KMP47C241N/M	5			1		4						•						•	28	28		•	•	
		KMP47C242BN	8					4				1		•						-	30			•	•	
		KMP47C243N/M/DM	8				1	8				1		•						•	28	28		•	•	<u>) </u>
	192	KMP47C221ADF KMP47C222N/F		24 20		1	1	4	H			1		•						井	42	44	Н	•		
		KMP47C400BN/BF	8	_		1	T	-4				,								-		44		•		<u>"</u>
		KMP47C410AN/AF			20	1																44		•	_	
		KMP47C412AN			20	1														-	42		Н			
		KMP47C421ADF		24		1														•		64		•		
		KMP47C422N/F		20			1	4				1		•					•	•		44	П	•	•	
		KMP47C423ADF		24		1						1			1					•		64				
	256	KMP47C433AN	8						1											•	42					
		KMP47C440BN/BF	8			1				8				•						•	42	44		•		
		KMP47C441AN/AF			16	1		4						•						•	42	44		•		
4k		KMP47C443N/M/DM	8				1	8				1		•						•	28	28		•	•)
		KMP47C446ADF		24		1		4						•					•			64				
		* [†] KMP47E486M												•						•		28		•	•	
		* [†] KMP47E487M												•						•		28		•	•)
		KMP47C407AN/AF				1						1				•				•	42	44		•	•	<u> </u>
		KMP47C451BN							L			1				•				-	30			•	•)
	768	KMP47C452BN/BF				1			L			1				•				-	42			•	•	<u>) </u>
		KMP47C453AN/AF				1						1				•				-	42	44		•	•	<u>) </u>
		KMP47C454AN							<u> </u>			1				•					30					
		KMP47C456ADF		32		1			<u> </u>			1		•		•			•			80				<u> </u>

^{*:} Under development †: USP4, 382, 279 owned by BULL CPU8

KLCS-47E Series

Product No	Function		ROM (bytes)	RAM (nibbles)	I/O port	Minimum instruction execution time (µs)	Power Supply Voltage (V)	Package	Built-in One-time PROM product	
KMP47C101P/M			1k	64	11			DIP16	KMP47P201VP	
KMP47C201P/M			2k	128		1.3	2.2 to 5.5	SOP16	1010 111 201 11	
KMP47C102P/M	LED driver		1k	64		1.5	(note2)	DIP20	YZN CO AFTOGOGY TO AT IN C	
KMP47C202P/M	LED WIVE			15			SOP20	KMP47P202VP/VM		
KMP47C206P/M		2k	128	10	1.0	4.0~5.7	DIP20 SOP20	TMP47P206VP/VM		
KMP47C103N/M	8-bit SIO		1k	64	23		2.2 to 5.5 (note2)	COMMO	KMP47P403VN/VM	
KMP47C203N/M	9_DIL 210				20	1.3		SDIP28 SOP28	MWIF47F400 V IV/ V IVI	
KMP47C241N/M	A/D converter		2k	128	21		2.7 to 6.0	501 20	KMP47P241VN/VM	
KMP47C243N/M/DM					23	1.0	2.2 to 5.5	SDIP28 SOP28	KMP47P443VN/VM/VDM	
KMP47C443N/M/DM	A/D converter 8-bit SIO		4k	256	20	1.0	(note 2)	SSOP30	INIXIA 41A 4490 VIVI VIVIV VIDIVI	
KMP47C222N/F	pulse output	LCD driver	2k	192	22	1.0	00 / 55	SDIP42	123M 00 4730 4993 733 AV70	
KMP47C422N/F		LCD driver	4k	256	44	(244)	2.2 to 5.5	QFP44	KMP47P422VN/VF	
* † KMP47E186M	E ² PROM (16byte), SPI		1k	64	11	1.3	2.0 to 5.5 (note 3)	SOP16	KMP47P186M(note 4)	
KMP47E187M	E ² PROM (16byte), SPI		1k	64	11	1.3	2.0 to 5.5 (note 3)	SOP16	KMP47P187M	
* † KMP47E486M	E ² PROM		4k	256	21	1.3	2.7 to 5.5	SOP28	KMP47W486M	
* † KMP47E487M	E ² PROM		4k	256	21	1.3	2.7 to 5.5	SOP28	KMP47W487M	

^{*:} Under development †: USP4, 382, 279 owned by BULL CPU8

Note1: (); the minimum instruction execution time when low-frequency clock is used. Note2: OTP built-in Type is under consideration for high-temperature range/high-quality

applications.

Note3: 2.7 to 5.5V when oscillator is connected, 2.0 to 3.4V during CR oscillation.

Note4: KMP47P186M (CR oscillation), KMP47P187M (oscillator version)

KLCS-47 Series

Product No	Func	ROM (bytes)	RAM (nibbles)	I/O port	Minimum instruction execution time (µs)	Power Supply Voltage (V)	Package	Built-in One-time PROM product		
KMP47C200BN/BF	Standard (LED o	lriver)	2k	128	36		2.7 to 6.0	SDIP42 QFP44	KMP47P400VN/VF	
KMP47C400BN/BF			4k	256			211 00 010	QFP44		
KMP47C210AN/AF			2k	128	36	1.9		SDIP42	KMP47P410AN/AF	
KMP47C410AN/AF	VFT driver		4k	256		1.0		QFP44	10/0 1/1 110/11/10	
KMP47C212AN	vri diva		2k	128	35			SDIP42	_	
KMP47C412AN			4k	256	33			2011 42		
KMP47C221ADF	LCD driver		2k	192					KMP47P421ADF	
KMP47C421ADF	LCD dilver	unver			28	1.9	18. 00	QFP64	MWF47F44LADF	
KMP47C423ADF		High-speed event counter	4k	256	20	2.0	4.5 to 6.0	42.00		
KMP47C231AN	D/A conversion (PWM) output	4-bit A/D conversion input, remote control pulse detector	2k	128	24			SDIP30 SDIP42	-	
KMP47C233AN	LED driver	3-bit A/D			36	1.0				
KMP47C433AN		conversion input	4k	256	30	1.9		501744		
KMP47C242BN		LED driver	2k	128	23		2.7 to 6.0	SDIP30	KMP47P242VN	
KMP47C440AN/AF	A/D converter.	LED anver			34			SDIP42	KMP47P440AN/AF	
KMP47C441AN/AF	watchdog timer	VFT driver		256	34		4.5 to 6.0	QFP44	KMP47P441AN/AF	
☆ KMP47C446ADF		LCD driver			24	1.9 (244)		QFP64	KMP47P446VDF	
KMP47C451BN					23	10.7		SDIP30	KMP47P451VN	
KMP47C452BN/BF			4k		OF.	16.7	2.2 to 6.0	SDIP42	KMP47P452VN/VF	
KMP47C453AN/AF	DTMF generator	-10.		35	8.3		QFP44	KMP47P453VN/VF		
KMP47C454AN	Divin generator		768	23			SDIP30	KMP47P454VN		
KMP47C407AN/AF					35	2.1	2.7 to 6.0	SDIP42 QFP44	KMP47P407VN/VF	
KMP47C456ADF		LCD driver]		34	8.3 (244)		QFP80	-	

Note: (); the minimum instruction execution time when low-frequency clock is used.

**These package types will be delivered in the short lead length package.

Type suffix

**P! Plastic dual in-line package (DIP)

N: Plastic shrink dual in-line package (SDIP)

F: Plastic flat package (QFP)

**G: Ceramic standard flat package (QFC)

■ KLCS-470A Series

Product No	Function	ROM (bytes)	RAM (nibbles)	I/O port	Minimum instruction execution time (µs)	Power Supply Voltage (V)	Package	Built-in One-time PROM product	
KMP47C623F	LCD driver (24 to 20×4),	6k	384	20 4- 20			QFP64	1771MD 47700003770	
KMP47C823F	High-speed event counter	8k	512	32 to 28			QFF04	KMP47P823VF	
KMP47C1220F	LCD driver (32×4),	12k	768	36			Organo	KMP47P1620VF	
KMP47C1620F	High-speed timer/counter	16k	108	30			QFP80	KIVIP4/PI020VF	
KMP47C637N		6k	384		1.3	15 4- 60			
KMP47C837N	On-screen display circuit, D/A conversion (PWM) output, 4-bit A/D conversion input, Remote control pulse detector	8k	304	- 32	(244)	4.5 to 6.0	SDIP42	YZNAD 4701 697 VN	
KMP47C1237N		12k						KMP47P1637VN	
KMP47C1637N		16k	E10						
KMP47C1238AN		12k	512	41			CDIDE4	Y/3.403.47701.6303.75.Y	
KMP47C1638AN		16k		41			SDIP54	KMP47P1638VN	
KMP47C853N/F		8k	1024	35	8.3 (244)	2.2 to 6.0		KMP47P853VN/VF	
KMP47C457N/F	DTMF generator	4k	768		0.1 (0.44)	0.5 . 20	SDIP42 QFP44	1771 AD 47700E775 JN 1 /5 JD	
KMP47C857N/F		8k	1024		2.1 (244)	2.7 to 6.0	Q	KMP47P857VN/VF	
KMP47C1260N/F	A/D converter (8bits×8ch),	12k					SDIP64		
KMP47C1660N/F	Remote control pulse detector, LED driver	16k		56			QFP64	KMP47P1660VN/VF	
KMP47C1270AN	VFT driver (16×18 to 13×16), D/A conversion (PWM)	12k	768		1.3 (244)	4.5 to 6.0	an India	173 MD 473D1 6703 733	
KMP47C1670AN	output, 4-bit A/D conversion input, Remote control pulse detector	16k		53			SDIP64	KMP47P1670VN	

note: (); the minimum instruction execution time when low-frequency clock is used.

Type N: Plastic shrink dual in-line package (SDIP) E: Ceramic shrink dual in-line package (SDIC) F: Plastic flat package (QFP) G: Ceramic standard flat package (QFC)

■ KLCS-47E, 47, 470 series (Wide-temperature range/High-quality products)

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Product No	Function	ROM (bytes)	RAM (nibbles)	I/O port	Minimum instruction execution time (µs)	Power Supply Voltage (V)	Operating temperature (°C)	Package	Built-in One-time PROM product (note 2)
* KMP47C101WP	LED driver	1k	64			2.2 to 5.5	-40 to 110	DIP16	TZN 400 450004110
* KMP47C201WP	LED unver	2k	128		1.3	(note 3)	-40 to 110	SOP16	KMP47P201VP
† KMP47E186M	E ² PROM (16 bytes), SPI	1k	64	11		(note 4)	-40 to 85	SOP16	KMP47P186M (note 5)
KMP47E187M	E ² PROM (16 bytes), SPI	1k	64			(note 4)	-40 to 85	SOP16	KMP47P187M
KMP47C241IN/IM	A/D converter	2k	128	21		27 to 60	-40 to 85	SDIP28	KMP47P241VN/VM
KMP47C241WM	LED driver	ZK.	120	21		2.7 to 6.0	-40 to 110	SOP28	KMP47P241VN
† KMP47E885IF	E ² PROM (64 bytes), PWM, UART A/D converter 16-bit timer/counter Input	8k	512	36		4.5 to 5.5	-40 to 85	QFP44	KMP47P885F
† KMP47E885WF	Capture, Output compare						-40 to 110		

* : Under development

†: USP4, 382, 279 owned by BULL CP8

Type suffix

P: Plastic dual in-line package (DIP)

M: Plastic small outline package (SOP)

N: Plastic shrink dual in-line package (SDIP)

F: Plastic flat package (QFP)

note 1: If there is any further information you require when considering I/W version products, please contact our sales representative.

note 2 : OTP built-in type is under consideration for high-temperature range/high-quality applications.

note 3: During CR oscillation (2.7 to 5.5V when oscillator is connected).

note 4: 2.7 to 5.5V when oscillator is connected, 2.0 to 3.4V during CR oscillation.

note 5: KMP47P186M (CR oscillation), KMP47P187M (oscillator version)